

RAMA INSTITUTE OF ENGINEERING & TECHNOLOGY, MANDHANA, KANPUR

Deptt. - Electronics & Communication Engg

ROOM NO.--207

(Odd-Semester: 2012-13)

B.Tech EC-V- Sem

Time –Table

w.e.f

	I	II	III	IV	12:20-01:10	V	VI	VII
	9:00-9:50	9:50-9:40	10:40-11:30	11:30-12:20		01:10-02:00	02:00-2:50	2:50-03:40
MON	EEC-501 (AS)	EEC502 (DP)	EHU-501 (MS)	EEC-504 (SKV)	H C N U L	EEC-503 (BT)	EIC-551 (CLS,DP)	
TUE	EEC-503 (BT)	EEC502 (DP)	EHU-501 (MS)	EEC-504 (SKV)		EIC-501 (CLS)	EEC-553 (BT, CLS)	
WED	EEC-501 (AS)	EEC502 (DP)	EHU-501 (MS)	EEC-504 (SKV)		EIC-501 (CLS)	EEC551(AS, BT)	
THU	EEC503 (BT)	EIC-501 (CLS)	EHU-501 (MS)	EEC-501 (AS)		EEC502 (DP)	EEC-552 (DP,BRS)	
FRI	EEC503 (BT)	EEC502 (DP)	EIC-501 (CLS)	EEC-504 (SKV)		EEC-501 (AS)	EEC503 (BT)(T)	EEC-504 (SKV)T
SAT	EEC-501 (AS)	EIC-501 (CLS)	EEC-501 (AS)T	EEC-504 (SKV)		EEC502 (DP)(T)	EEC503 (BT)	EIC-501 (CLS)(T)

EEC-501: Integrated Circuits (AS: Mr. Abhishek Srivastava) 6

EEC-502: Principles of Communication (DP : Mr. Deepak Patel) 6

EIC-501: Control System (CLS : Mr. C L Sahu) 6

EEC-503: Microprocessors (BT: Mr. Brijesh Tyagi) 6

EEC-504: Antenna & Wave Propagation (SKV :Mr. Sunil Kumar Verma) 6

EHU-501: Engg & Managerial Economics(MS:Ms. Mahima Shukla) 4

EEC-551: Integrated Circuit Lab (AS) 2

EEC-552: Communication Lab-1(DP) 4

EIC-551: Control System Lab (CLS) 4

EEC-553: Microprocessors Lab (BT) 4

EEC-552: Communication Lab-1(BRS) 2

(Deepak Patel)
Time Table in-charge

(B R Singh)
HOD EC